



**LAKIREDU BALI REDDY COLLEGE OF ENGINEERING (AUTONOMOUS)**  
**L.B. Reddy Nagar :: Mylavaram-521 230 :: Krishna Dist. :: A.P**  
 Approved by AICTE, New Delhi. Affiliated to JNTUK, Kakinada  
 Accredited by NBA, Accredited by NAAC with "A" Grade & ISO 9001: 2008 Certified  
**EXAMINATION SECTION**

**M.Tech.(I-Semester) - I Mid Examinations – A.Y. 2016-2017**  
 (2016-17 Admitted Batch)

**REVISED**

**TIME TABLE**

DATE	TIME	Computer Science and Engineering	Software Engineering	VLSI and Embedded Systems	Thermal Engineering	Power Electronics and Drives
13-10-2016 Thursday	9.30 AM – 11.30 AM	MTC101 - Advanced Data Structures	MTIT101 - Advanced Problem Solving	MTVL101 - VLSI Technology and Design	MTME101 - Mathematical Methods in Engineering	MTEE101 - Machine Modeling and Analysis
	2.15 PM – 04.15 PM	MTC102 - Advanced Database Management Systems	MTIT102 - Software Architecture	MTVL102 - CPLD and FPGA Architectures and Applications	MTME102 - Advanced Thermodynamics	MTEE102 - Power Converters
14-10-2016 Friday	9.30 AM – 11.30 AM	MTC103 - Data Mining	MTIT103 - Software Process Management	MTVL103 - Microcontrollers for Embedded System Design	MTME103 - Advanced Heat & Mass Transfer	MTEE103 - Control of Motor Drives-I
	2.15 PM – 04.15 PM	MTC104 - Advanced Computer Networks	MTIT104 - Object Oriented Software Engineering	MTVL104 - Embedded Real Time Operating Systems	MTME104 - Advanced IC Engines	MTEE104 - Computational Mathematics
15-10-2016 Saturday	9.30 AM – 11.30 AM	<b>Elective-I</b> MTC1054 - Software Project Management	<b>Elective - I</b> MTIT1052 - Mobile Computing	<b>Elective - I</b> MTVL1051 - Wireless Communications & Networks	<b>Elective - I</b> MTME1051 - Advanced Fluid Mechanics	<b>Elective - I</b> MTEE1052 - Reliability Systems Engineering
	2.15 PM – 04.15 PM	<b>Elective-II</b> MTC1063 - Artificial Intelligence	<b>Elective - II</b> MTIT1063 - Digital Image Processing	<b>Elective - II</b> MTVL1061 - Modern DSP	<b>Elective - II</b> MTME1061 - Advanced Power Plant Engineering	<b>Elective - II</b> MTEE1062 - Modeling and simulation of Power Electronics

Date: 28-09-2016

*Am*  
CoE

*DM*  
DIRECTOR

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